EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
LI	9	US-20020008543-\$.DID. OR US-20020153844-\$. DID. OR US-20020153844-\$. DID. OR US-20030062524-\$.DID. OR US-20040227749-\$.DID. OR US-20050052209-\$. DID. OR US-20060187730-\$.DID. OR US-68525574-\$.DID. OR US-6633136-\$.DID. OR	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
12	31	(("20030006713") or ("2003000685") or ("20030048669") or ("20030128199") or ("20030128200") or ("20030128200") or ("2003012820") or ("20030124465") or ("20030214465") or ("20030214465") or ("20040041752") or ("200400408166") or ("2004008166") or ("2004008165029") or ("20040207615") or ("20050218821") or ("20050218821") or ("20050218821") or ("20050218821") or ("20050218821") or ("6305089") or ("6310589") or ("6310589") or ("6310589") or ("6363368") or ("6839365") or ("6839365") or ("6983826") .PN.	US-PGPUB; USPAT	OR	OFF	2010/11/18
L3	16815	kimura.inv.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49

L4	11	L2 and L3	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L5	42899	precharge (pre near5 charge) pre-charge	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L6	0	L4 and L5	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L7	4957	345/204.ccls.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L8	4474	345/76-81.ccls.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L9	42899	precharge (pre near5 charge) pre-charge	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L10	374	L8 and L9	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L11	251	L7 and L9	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L12	38	"6310589"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L13	22	"5521871"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L14	9	"5610549"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L15	16815	kimura.inv.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L16	62	L15 and (driven near5 circuit).clm.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L17	104787	"345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L18	5	L16 and L17	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L19	177	"6373454"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L20	113957	(diode near5 transistor)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L21	1137	(diode adj connect)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L22	104787	"345".clas.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L23	4044	L20 and L22	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L24	145	L21 and L22	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L25	1	(diode adj connect) with L5	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49

L26	843	diode with L5	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L27	121	L22 and L26	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L28	2	"20030231152"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L29	463	(plural multiple) with L5 with circuit	US-PGPUB; OR USPAT		ON	2010/11/18 16:49
L30	31	L22 and L29	US-PGPUB; USPAT			2010/11/18 16:49
L31	1	"6483494".pn.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L32	1	"20030006955"	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L33	128	(plural multiple) near5 precharge near5 circuits	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L34	2969	(plural multiple) near5 current near5 sources	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L35	6	L33 same L34	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L36	42899	precharge (pre near5 charge) pre-charge	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L37	527	(plural multiple) near5 L36	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L38	8	L37 same L34	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L39	15	L37 and L34	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L40	***************************************	["20020180369" "20020189964" "20030016190" "20030020705" "20030020705" "20050286625" "20050286625" "20070262978" "20080197777" "6870553" "6992663" "7345657" "7453427"). PN.	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L41	1413	(first near3 transistor) near3 (larger greater) near3 (second near3 transistor)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49

L42	618	(first near3 transistor) near3 (smaller lesser) near3 (second near3 transistor)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L43	20	L41 and (L7 L8)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L44	9	L42 and (L7 L8)	US-PGPUB; USPAT	OR	ON	2010/11/18 16:49
L45	71	(gate near3 width near3 first near3 transistor) near3 (larger greater) near3 (gate near3 width near3 second near3 transistor)	US-PGPUB; OR USPAT		ON	2010/11/18
L46	354	(current adj source) same pre\$1charge same switch	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L47	62	(L7 L8) and L46	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L48	100	L17 and L46	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L49	38	L48 not L47	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L50	658	(current adj source) same pre\$1charge same transistor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L51	36	(L7 L8) and L50	US-PGPUB; OR USPAT; EPO; JPO		ON	2010/11/18 16:49
L52	59	L17 and L50	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L53	2	US-5552677-\$.DID. OR US-6839057-\$.DID.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L54	2	"7079125".pn. "6924787". pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L55	4	("20020008687" "20020050972" "6304241" "6924782"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2010/11/18 16:49
L56	632	pre\$1charge same shar\$3 same ((data source) near3 line)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49

L57	283	pre\$1charge with shar\$3 with ((data source) near3 line)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L58	655	pre\$1charge with "same" with ((data source) near3 line)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L59	122	L22 and L58	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L60	51	(pre\$1charge near3 transistor) with (shar\$3 "same") with ((data source) near3 line)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:49
L61	16040	345/204-215.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:57
L62	4550	345/690-699.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 16:58
L63	28474	345/55-59,76-104.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/11/18 17:00

EAST Search History (Interference)

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L64	8196	345/204-215.ccls.	USPAT; UPAD	OR	ON	2010/11/18 17:01
L65	2628	345/690-699.cds.	USPAT; UPAD	OR	ON	2010/11/18 17:01
L66	12230	345/55-59,76-104. ccls.	USPAT; UPAD	OR	ON	2010/11/18 17:01
L67	0	(gate near3 (width length) near3 (smaller larger) same (first and second) near3 transistor same pre \$1charge).clm.	USPAT; UPAD	OR	ON	2010/11/18 17:03
L68	0	(gate near3 (width length) with (smaller larger) same (first and second) near3 transistor same pre \$1charge).clm.	USPAT; UPAD	OR	ON	2010/11/18 17:04

L69		(gate near3 (width length) near3 (smaller larger) same (first and second) near3 transistor same pre \$1charge)	USPAT; UPAD	OR	ON	2010/11/18 17:04
L70	1	(gate near3 (width length) with (smaller larger) same (first and second) near3 transistor same pre \$1charge)	USPAT; UPAD	OR	ON	2010/11/18 17:04

11/18/2010 5:41:39 PM

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